WEST

| Help | Logout | Interrupt |

| Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Preferences | Cases |

Search Results -

Term	Documents
MEMORY	1366909
MEMORIES	160956
MEMORYS	177
RESERVED	71477
RESERVEDS	0
ADDRESS	565212
ADDRESSES	236206
((RESERVED ADJ2 ADDRESS) SAME 1 SAME MEMORY).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	7
(L1 SAME MEMORY SAME (RESERVED ADJ2 ADDRESS)).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	7

Database:
```
US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:
```
(arbiter or arbitration) same (priority
or prioritize) same (request or access)
same (reserved adj2 address)
```

| Refine Search |

| Recall Text | | Clear |

Search History

DATE: Friday, November 21, 2003   Printable Copy   Create Case

Set Name	Query	Hit Count	Set Name
side by side			result set
	DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L4	l1 same memory same (reserved adj2 address)	7	L4
L3	l1 and l2	7	L3
L2	(detect or detecting or determine or determining) same (access or request) same memory same (reserved adj2 address)	18	L2
L1	(monitor or monitoring) same (signal or address or information) same bus	26277	L1

END OF SEARCH HISTORY

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| Help | Logout | Interrupt |

| Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Preferences | Cases |

Search Results -

Term	Documents
ARBITER	9401
ARBITERS	1256
ABITRATOR	2
ABITRATORS	0
ARBITRATION	17216
ARBITRATIONS	313
ARBITRATING	5190
ARBITRATINGS	0
PRIVATE	83822
PRIVATES	233
NON	2863270
((ARBITER OR ABITRATOR OR ARBITRATION OR ARBITRATING) SAME ((PRIVATE OR (NON ADJ SHARED) OR RESERVED OR RESTRICTED) ADJ4 (MEMORY OR REGION OR AREA)) SAME MEMORY).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	41

There are more results than shown above. Click here to view the entire set.

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

(arbiter or arbitrator or arbitration or
arbitrating) same ((private or (non adj
shared) or reserved or restricted) adj4

| Refine Search |

| Recall Text | | Clear |

Search History

DATE: **Friday, November 21, 2003**   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L22	(arbiter or abitrator or arbitration or arbitrating) same ((private or (non adj shared) or reserved or restricted) adj4 (memory or region or area)) same memory	41	L22
L21	L9	1	L21
	DB=USPT; PLUR=YES; OP=ADJ		
L20	6006319.pn.	1	L20
L19	5991230.pn.	1	L19
L18	5991230.pn.	1	L18
L17	5933855.pn.	1	L17
L16	5844856.pn.	1	L16
L15	5933855.pn.	1	L15
L14	5991230.pn.	1	L14
L13	6006319.pn.	1	L13
L12	6014729.pn.	1	L12
L11	6032232.pn.	1	L11
L10	6035360.pn.	1	L10
	DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L9	L8 and l7	1	L9
L8	mclean.xa.	140	L8
L7	super adj agent	45	L7
L6	mclean.exam	0	L6
L5	mclean.exm	0	L5
L4	(arbiter or arbitration or controller or arbitrator) same (request or access) same memory same (reserved adj2 address)	17	L4
L3	(arbiter or arbitration) same (request or access) same memory same (reserved adj2 address)	5	L3
L2	(arbiter or arbitration) same (request or access) same (grant or priority or prioritize) same memory same (reserved adj2 address)	1	L2
L1	(arbiter or arbitration) same (request or access) same (priority or prioritize) same memory same (reserved adj2 address)	0	L1

END OF SEARCH HISTORY

WEST

☐ | Generate Collection | | Print |

L9: Entry 16 of 19   File: USPT   Apr 5, 1994

US-PAT-NO: 5301328
DOCUMENT-IDENTIFIER: US 5301328 A

TITLE: System and method for shadowing and re-mapping reserved memory in a microcomputer

DATE-ISSUED: April 5, 1994

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Begur; Sridhar	San Jose	CA		
Jones, Jr.; Irvin R.	San Jose	CA		

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Hewlett-Packard Company	Palo Alto	CA			02

APPL-NO: 07/ 951650   [PALM]
DATE FILED: September 25, 1992

PARENT-CASE:
CROSS REFERENCE TO RELATED APPLICATION(S) This is a divisional of copending application Ser. No. 07/472,057 filed on Jan. 31, 1990, now U.S. Pat. No. 5,202,994.

INT-CL: [05] G06F 12/02

US-CL-ISSUED: 395/700; 395/400, 364/DIG.1, 364/280, 364/280.9, 364/281.1
US-CL-CURRENT: 711/201; 711/118, 711/170, 711/202

FIELD-OF-SEARCH: 395/700, 395/400

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

| Search Selected | | Search ALL |

	PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
☐	4459658	July 1984	Gabbe et al.	364/200
☐	4475176	October 1984	Ishii	364/900
☐	4564922	January 1986	Muller	364/900
☐	4713756	December 1987	Mackiewicz et al.	364/200
☐	5237669	August 1993	Spear	395/400

ART-UNIT: 236

PRIMARY-EXAMINER: Shaw; Gareth D.

ASSISTANT-EXAMINER: Chaki; Kakali

ABSTRACT:

A system and method for managing the reserved memory in a microcomputer copies selected portions of reserved memory to a new reserved memory having a faster access time, and allows any free portions of the new reserved memory to be accessed by a typical software application. After the selected portions of reserved memory are copied, all access to an address within a selected portion are re-directed to the new reserved memory. Any free portions of new reserved memory have additional, accessible memory re-mapped to these free portions.

7 Claims, 6 Drawing figures